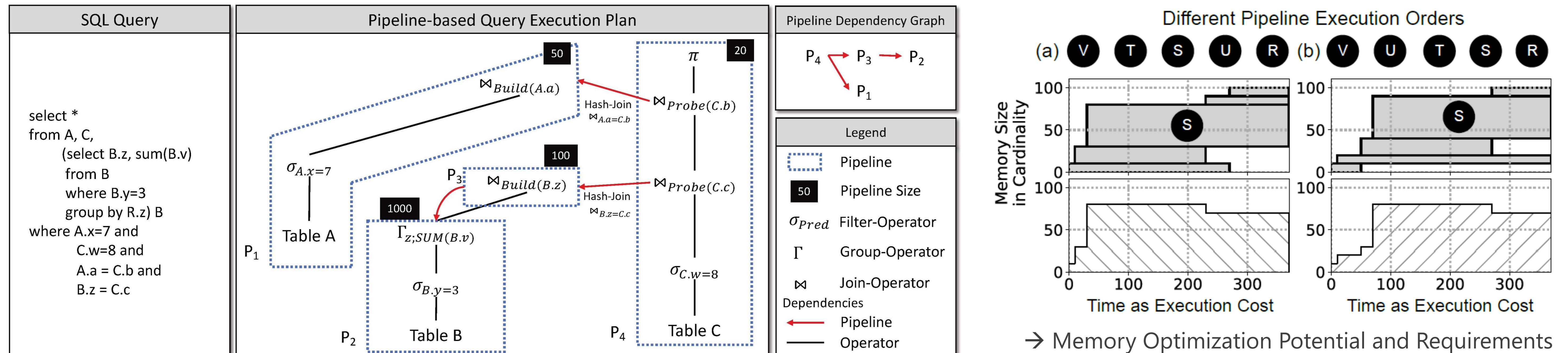
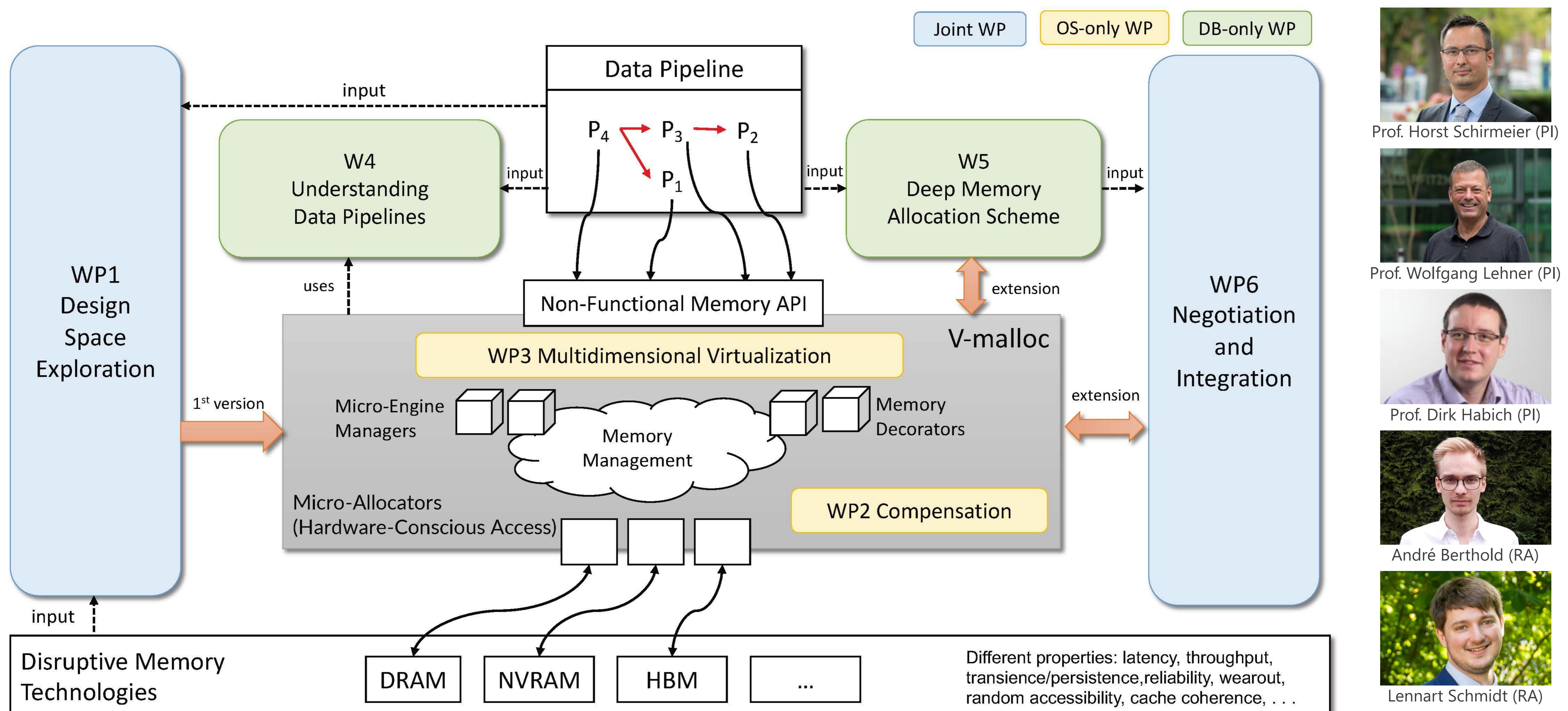


Data Pipelines



Overall Project Structure and Project Members



Project Details

Phase 1 (Single-Query Pipeline Scheduling)

- DB: Efficient algorithms for “heterogeneous memory pipeline orderings” considering NFPs
- OS: Abstractions for heterogeneous memories; virtualization + compensation, transparent migration/replacement

Phase 2 (Multi-Query Pipeline Scheduling)

- Global schedule + compensation plan
- Negotiation at query compile time

Current System Under Test (SUT)

- Xeon Phi 7250
- NUMA-Nodes
- Heterogeneous Memory
 - DRAM
 - HBM

